RONETIX
DEVELOPMENT TOOLS

i.MX8M-MB

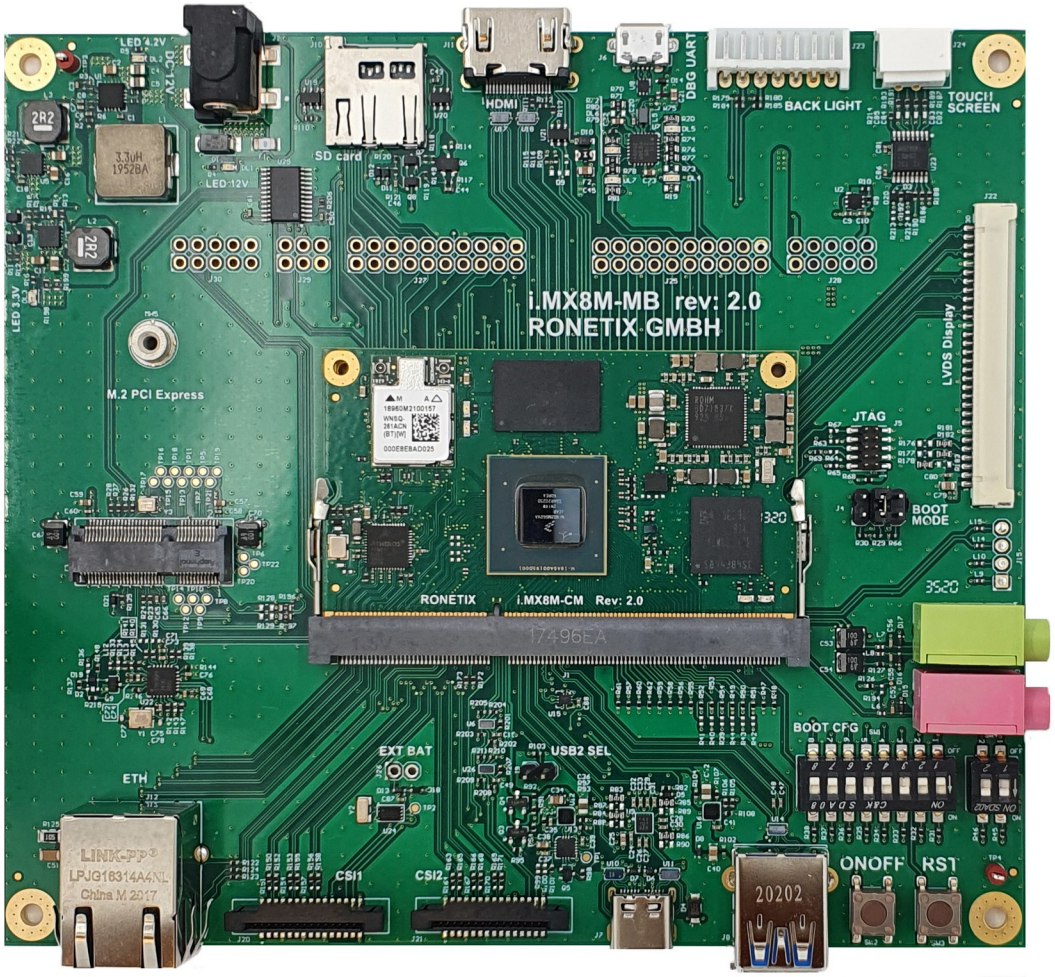
Base board for the CPU Modules:

i.MX8M-CM

i.MX8M-MINI-CM

I.MX8M-NANO-CM

Datasheet, rev 1.0



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1. Document Revision History

Revision	Date	Notes
1.0	14-Jul-2021	Initial release

2. Table of Contents

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3. Overview

3.1 General Information

The **i.MX8M-MB** base board is designed to explore the functionality and performance of the CPU Modules i.MX8M-CM, i.MX8M-MINI-CM and i.MX8M-NANO-CM.

3.1.1 Reference Documents

i.MX8M-MB – Base board downloads:

http://download.ronetix.at/boards/doc/i.MX8M-MB/

i.MX8M-CM – CPU Module downloads:

http://download.ronetix.at/boards/doc/i.MX8M-CM

3.1.2 Board Layout

The I.MX8M-MB physical dimensions are 147.0 x 130.5 mm.

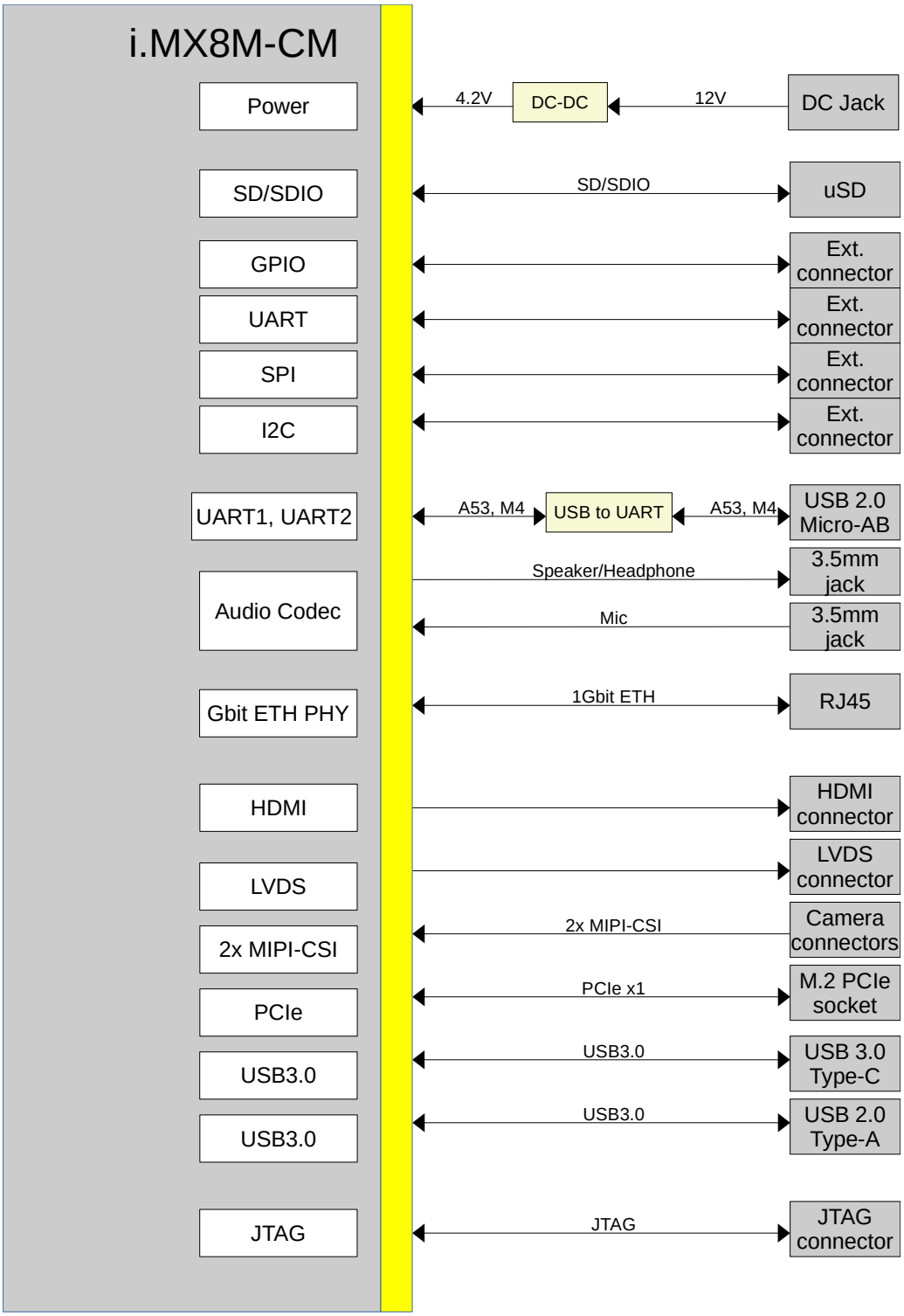
3.2 Highlights

The i.MX8M-MB base board provides the following features and interfaces:

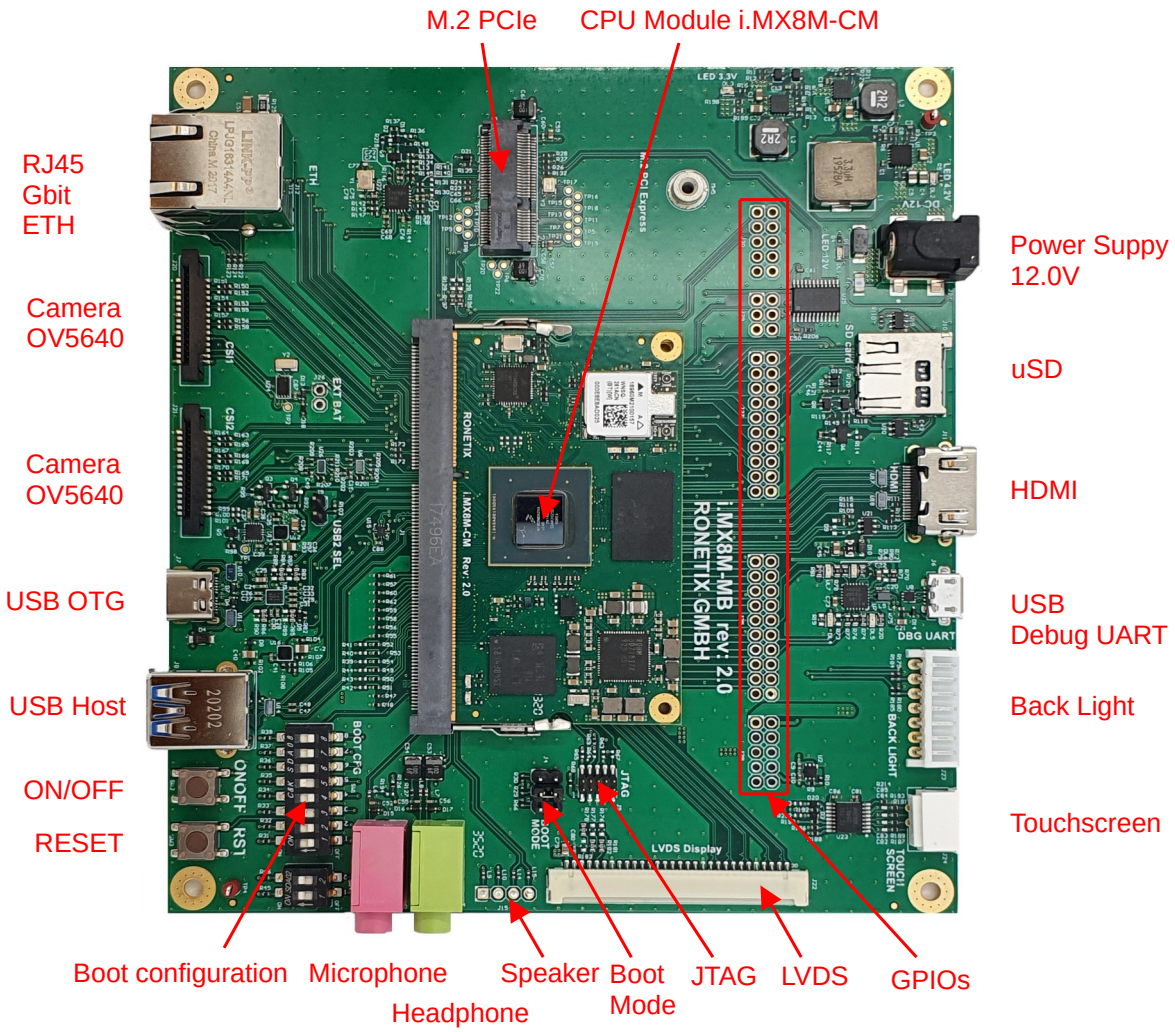
- SODIMM204 connector compatible to i.MX8M-CM
- Ethernet 10/100/1000BaseT – RJ45
- USB3.0 Host Type A
- USB3.0 Type C
- USB debug UART1 and UART2
- micro-SD card slot
- M.2 PCIe Connector
- HDMI Display interface
- LVDS Display interface
- Touch panel interface
- Camera – MIPI-CSI for OV5640
- Audio
  - 3.5mm Headphones jack
  - 3.5mm Microphone jack
  - 1W Speakers
- GPIO headers

Refer to i.MX8M-CM datasheet for pin-out assignments.

3.3 Block Diagram



3.4 i.MX8M-MB Connectors



Reference	Function	Type
J1	i.MX8M-CM connector	SODIMM204
J2	Power Supply	DC Jack, 2.1mm
J3	M.2 PCIe	MDT420E01001
J4	Boot Mode	Header 3x2, 2.54mm
J5	JTAG	Header 5x2, 1.27mm
J6	USB Debug UART	USB Micro-AB
J7	USB 3.0 OTG	USB 3.0 Type C

Reference	Function	Type
J8	USB Host	USB Type-A
J9	USB2 switch	Jumper 2.54mm
J10	Micro SD	Micro SD
J11	HDMI	HDMI
J12	Ethernet	RJ45
J13	Microphone - Pink	Audio jack 3.5mm
J14	Headphones - Green	Audio jack 3.5mm
J15	Speakers	Header 2x2, 2.54mm
J20	Camera OV5640	15-pin, FPC
J21	Camera OV5640	15-pin, FPC
J22	LVDS	30pin, DF14-30P-1.25H
J23	Display back light	7-pin, S7B-EH
J24	Touchscreen	4-pin, FFC
J25	GPIOs	Header 10x2, 2.54mm
J26	RTC clock external battery	Header 1x2, 2.54mm
J27	GPIOs	Header 10x2, 2.54mm
J28	Power supplies	Header 5x2, 2.54mm
J29	SPDIF	Header 3x2, 2.54mm
J30	GPIOs	Header 5x2, 2.54mm

4. Interface description

4.1 Power supply

4.1.1 Power supply input

i.MX8M-MB is powered by a +12V power supply connected through a 2.1mm barrel jack.

4.1.2 Power out headers

- **J28**, 2x5 2.54mm header provides supplies generated on the base board.

J28, PIN	Label	Voltage	Description
1	VIN_4V2	4.2V	Power supply provided to SODIMM204
2	CM_VDD	3.3V/1.8V	Provided from SoM, generated by PMIC: 3.3V/1.5A – i.MX8M-CM 1.8V/0.5A – i.MX8M-MINI-CM
3, 4	VCC_3V3	3.3V	Generated from 12V through 3A DC-DC converter
5	VCC_5V0	5.0V	Generated from 12V through 3A DC-DC converter
9	VCC_1V8	1.8V	Generated from VCC_3V3 by a 0.3A Linear regulator
6, 8, 9, 10	GND		

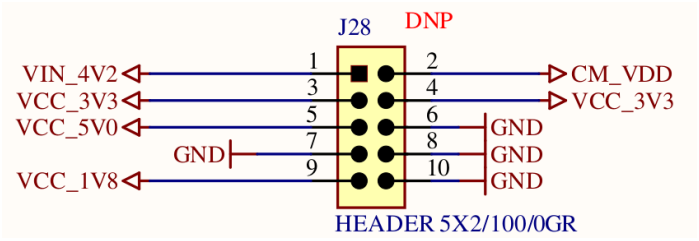


Figure 4.1: Power out connector

4.2 Buttons

i.MX8M-MB implements two buttons.

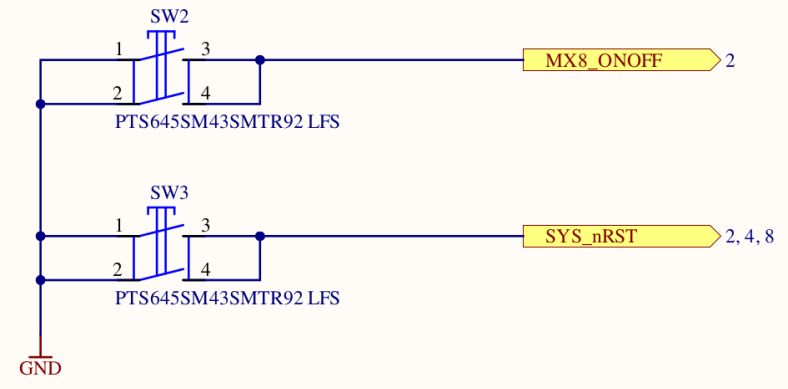


Figure 4.2: Buttons

4.2.1 ON/OFF button

The ON/OFF button provides following functionality:

- Long press (> 5 sec.) - hardware shutdown without possibility for software wake-up
- Short press (< 5 sec.) - software shutdown
- Short press in OFF state – restart

4.2.2 RST button

The RST button performs a system reset.

4.2.3 Boot Mode

J4 sets the boot mode of the CPU:

- i.MX8M and i.MX8M-MINI

J4, 3-4	J4, 1-2	Boot mode – I.MX8M, I.MX8MM
open	open	Boot from Fuses
open	close	Serial download
close	open	Internal boot
close	close	Test mode

- i.MX8M-NANO

J4, 3-4	J4, 1-2	Boot mode – I.MX8MN
open	open	Boot from Fuses
open	close	Serial download
close	open	uSDHC3, eMMC
close	close	uSDHC2, Micro SD

4.2.4 Boot configuration

Boot configuration switches SW1, SW4 set the boot source and sequence:

BOOT from SD2 card i.MX8M, i.MX8MM:
SW1: 3,5 - ON; all other OFF
SW4: 1,2 - ON

BOOT from eMMC i.MX8M (SDHC1):
SW1: 6 - ON; all other OFF
SW4: 1, 2 - ON

BOOT from eMMC i.MX8MM (SDHC3):
SW1: 4,6 - ON; all other OFF
SW4: 1, 2 - ON

Figure 4.3: Boot configuration i.MX8M and i.MX8MM

BOOT from SD card i.MX8MN (SDHC2):
BOOT_MODE = 011
SW1: doesn't matter
SW4: doesn't matter

BOOT from eMMC i.MX8MN (SDHC3):
BOOT_MODE = 010
SW1: doesn't matter
SW4: doesn't matter

Figure 4.4: Boot configuration i.MX8MN

4.3 LEDs

LED	Color	Description
DL1	green	ON when input 12V DC power is provided
DL2	red	ON when VIN_4V2 is available
DL3	red	ON when VCC_3V3 is available
DL4	green	RxLED on USB debug UART2 bridge
DL5	red	TxLED on USB debug UART2 bridge
DL6	green	RxLED on USB debug UART1 bridge
DL7	red	TxLED on USB debug UART1 bridge

4.4 Ethernet

One Gigabit Ethernet port is available: RJ45 connector J12 with integrated magnetics and LEDs.

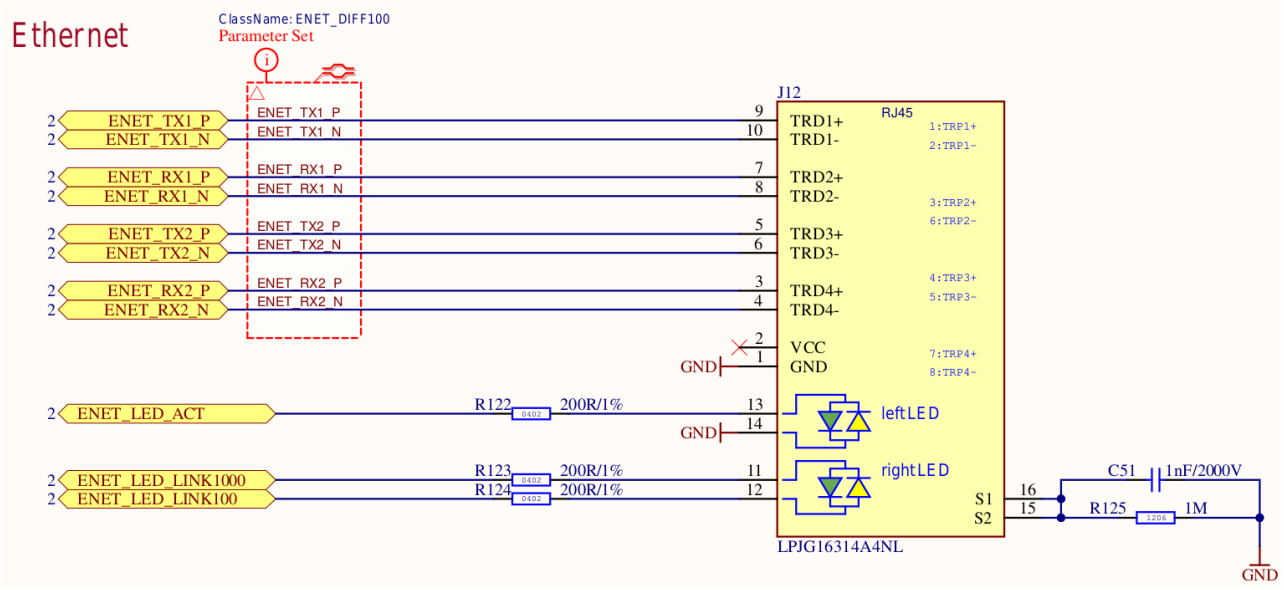


Figure 4.5: RJ45

4.5 USB Interface

The i.MX8M-CM features two USB3.0 ports, i.MX8M-MINI-CM has two USB2.0 ports and i.MX8M-NANO-CM has one USB2.0 port.

On the base board the first USB port is connected to a USB3.0 Type C connector – J7,

the second port is connected to a USB3.0 Host Type A connector – J8.

4.5.1 USB 3.0 OTG Type C, J7

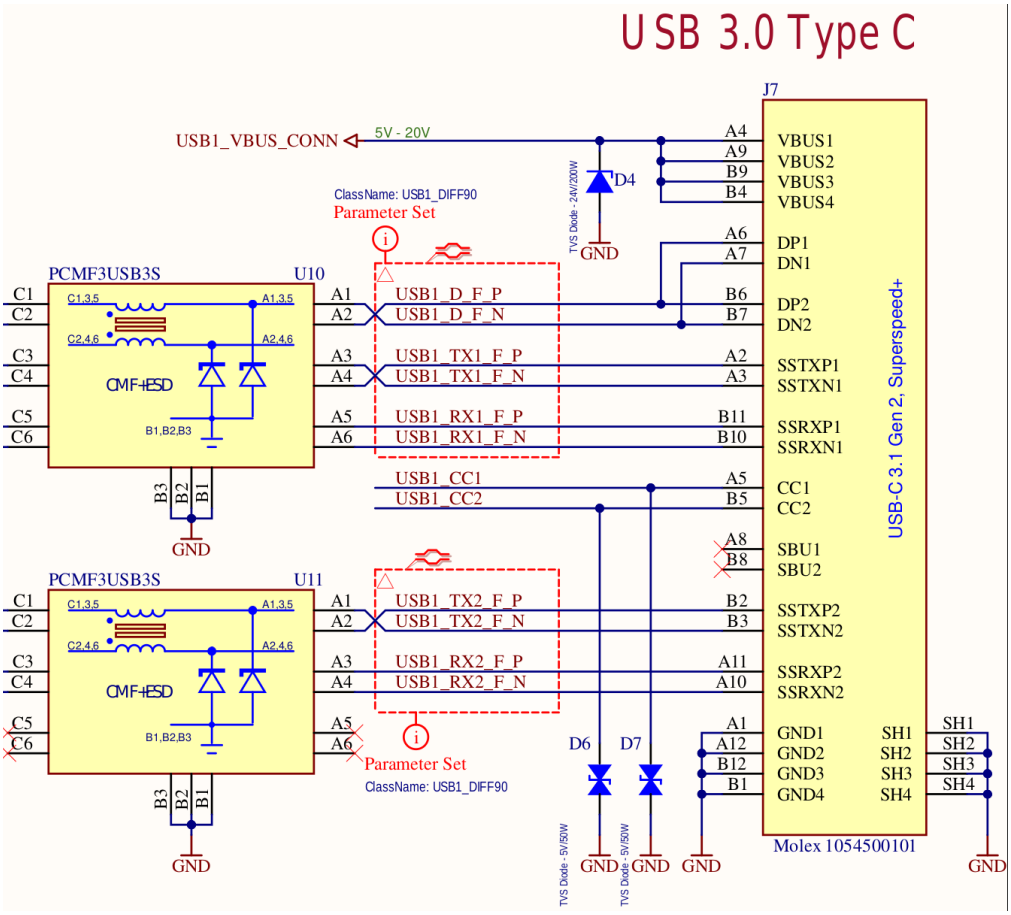


Figure 4.6: USB3.0 Type C

4.5.2 USB 3.0 Host Type A, J8

The USB2.0 signals can be multiplexed to J8 or to the WiFi module on SoM (through SODIMM204 connector).

J9	Multiplex mode
open	USB2_D routed to BT module on i-MX8-CM
close	USB2_D routed to USB 3.0 Host Type A connector (default)

4.6 SD card

The i.MX8M-MB board features a SD card socket which can be used as boot device or storage. Please note that the hardware supported card detect function is implemented and hardware write protect feature is not available.

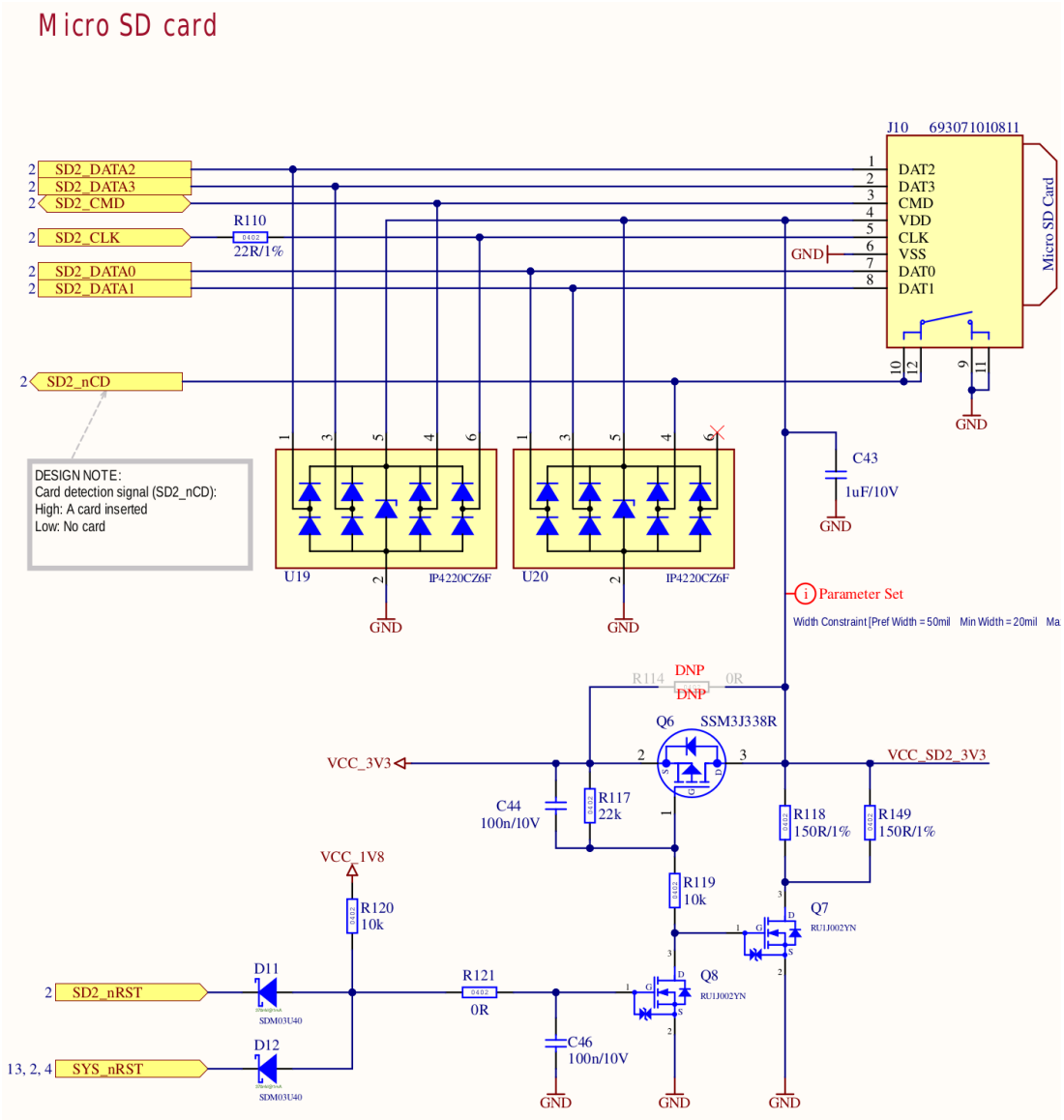


Figure 4.7: SD card

4.7 HDMI

The i.MX8M-MB board exposes the HDMI function of i.MX8M-CM.

iMX8M-MINI-CM and i.MX8M-NANO-CM don’t implement HDMI interface.

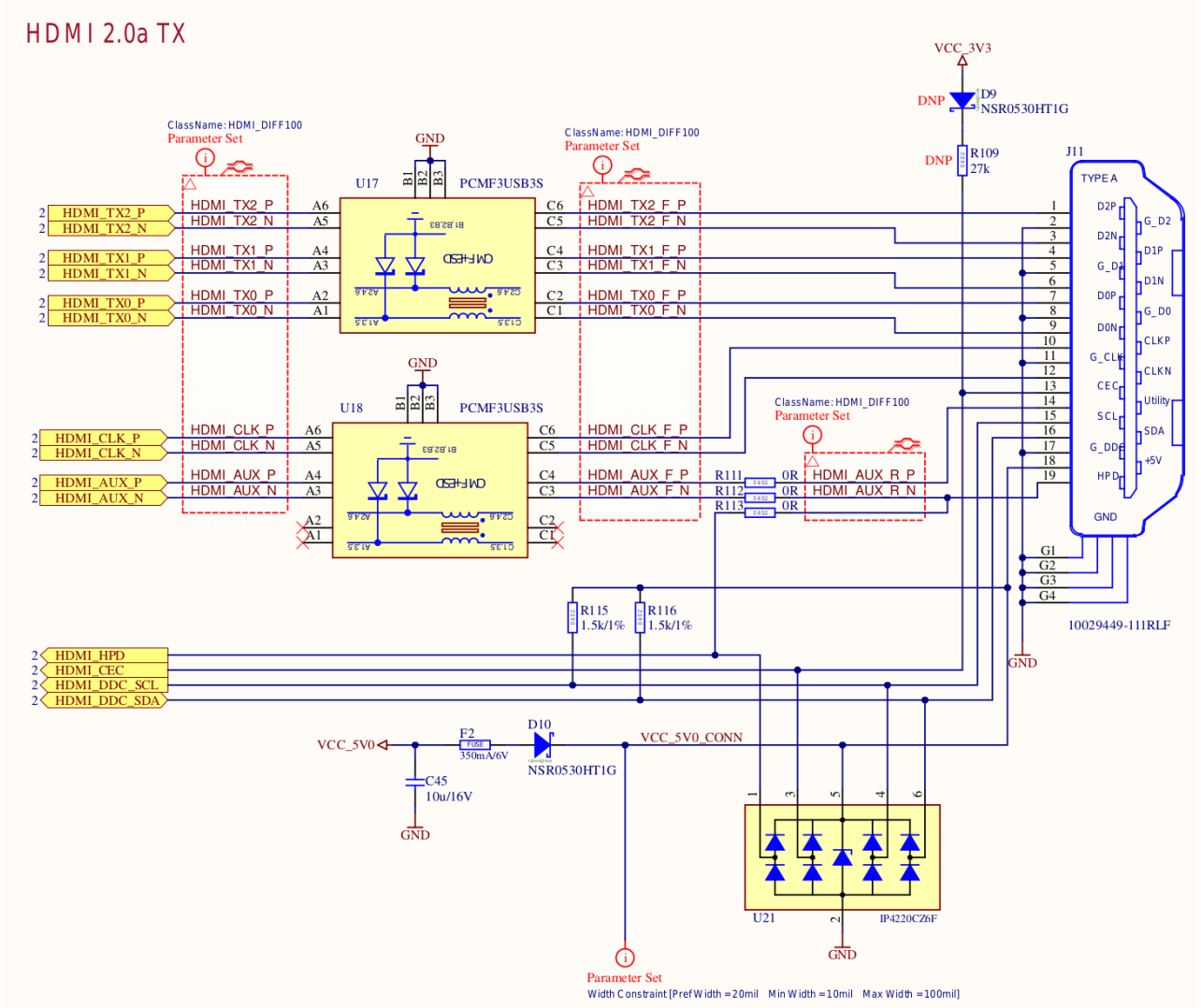


Figure 4.8: HDMI

4.8 LVDS

The i.MX8M-MB board exposes a 4 data lane LVDS interface driven by the MIPI-DSI to LVDS bridge assembled on the i.MX8M-CM. The interface is exposed to a 30 pin connector for connecting G101ICE-L01, 10.1", 1280x800 LVDS LCD display.

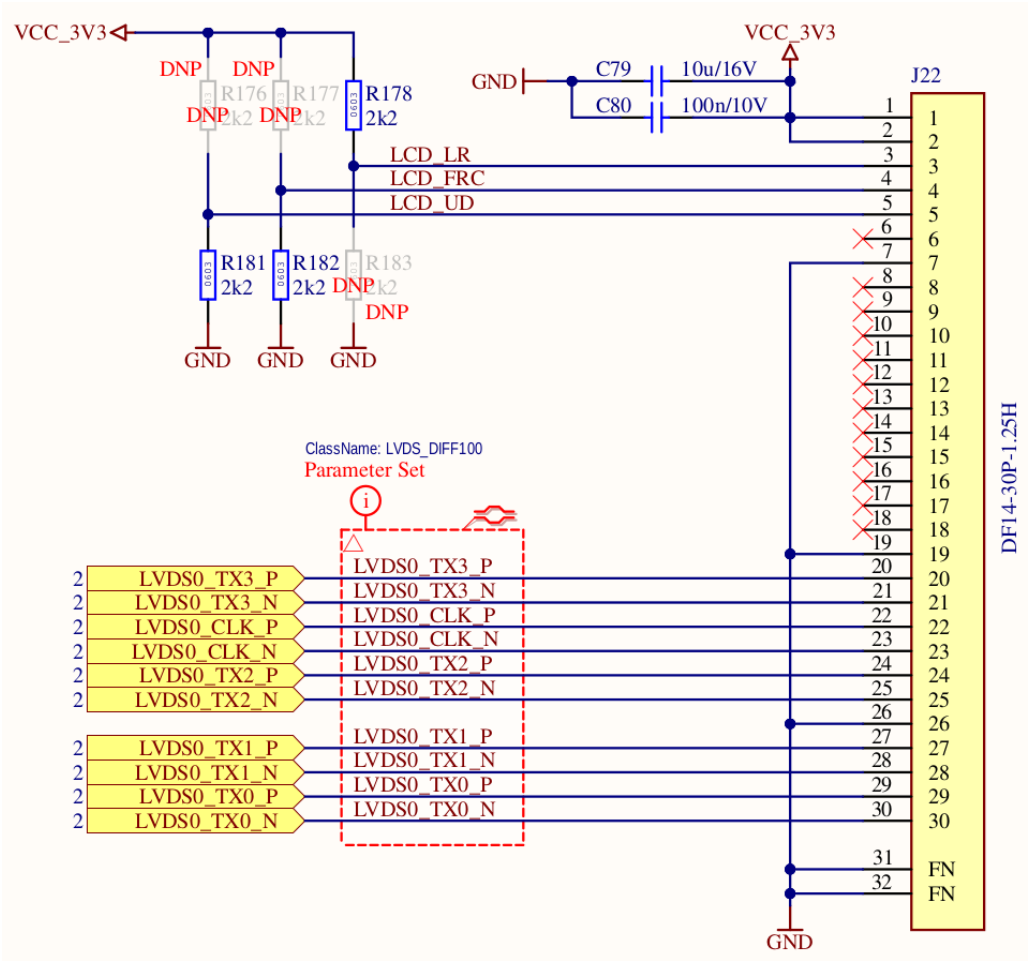


Figure 4.9: LVDS

4.9 Touchscreen

The i.MX8M-MB board provides a resistive touchscreen interface (J24) through TI TSC2046.

4.10 Audio

The i.MX8M-MB board features two 3.5mm jacks:

- Headphones

- Header 2x2, 2.54mm for speakers
- Microphone

Note: The using of headphones or speakers is an assembling option of i.MX8M-CM. It is not possible to use both headphones and speaker simultaneously.

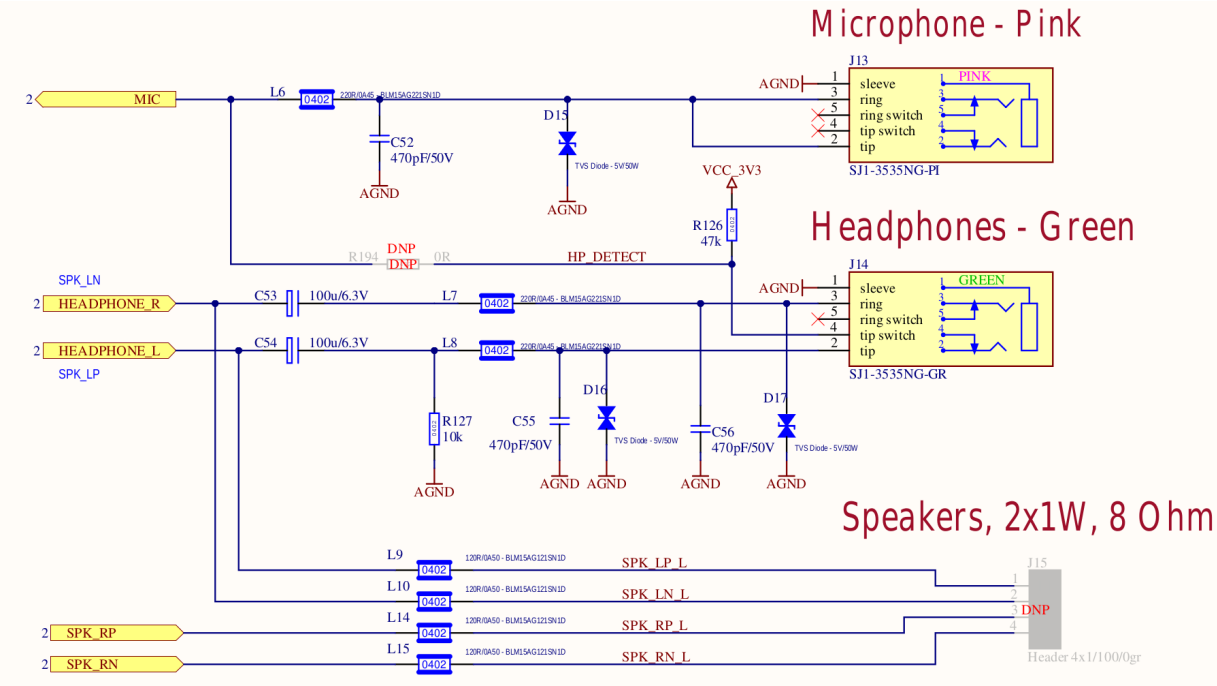


Figure 4.10: Audio

4.11 Camera

The i.MX8M-MB supports two camera sensor inputs using two 15-pin FPC connectors which are suitable for PCAM 5C, 5 MP MIPI camera module with OV5640 sensor.

On i.MX8M-CM CSI1 and CSI2 are available, while on i.MX8M-MINI-CM and i.MX8M-NANO-CM only CSI1 is available.

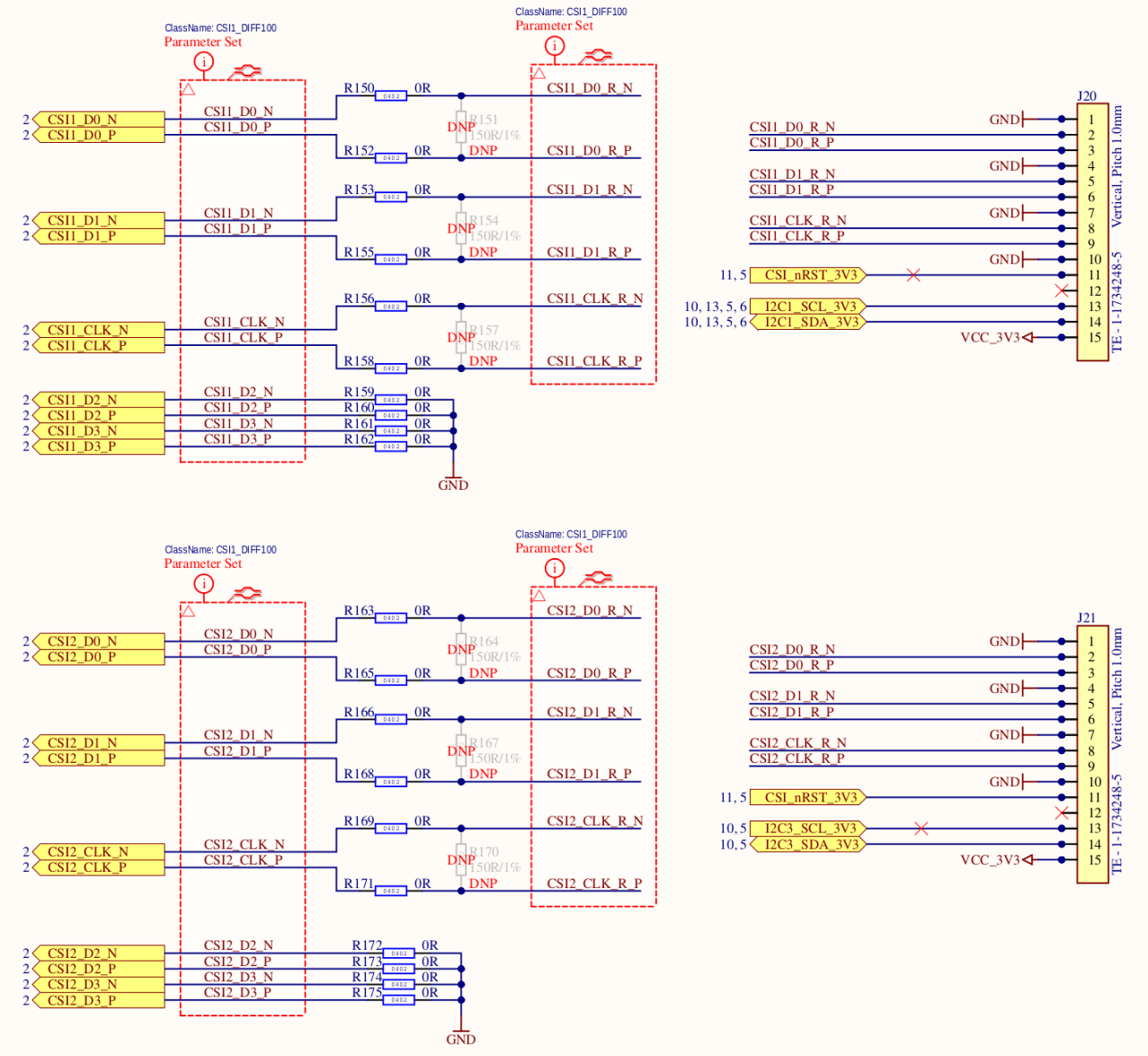


Figure 4.11: Camera

4.12 USB debug UART

i.MX8M-MB implements a USB to UART bridge (CP2105) which can be used to interface with the UART1 and UART2 of the CPU. Usually UART1 is used for debugging of Cortex-M4 core, while the UART2 is used for debugging of Cortex-A53 core. J6, USB Micro-AB connected is connected to the USB-UART bridge. Four LEDs show Tx/Rx activity on both UART ports.

4.13 GPIO

Various GPIOs are available on standard headers:

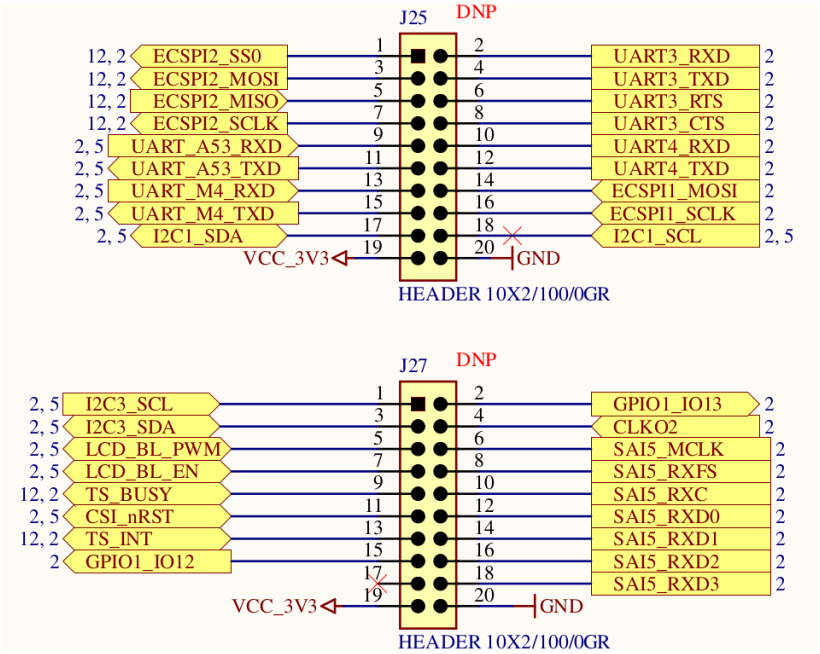


Figure 4.12: GPIO-1

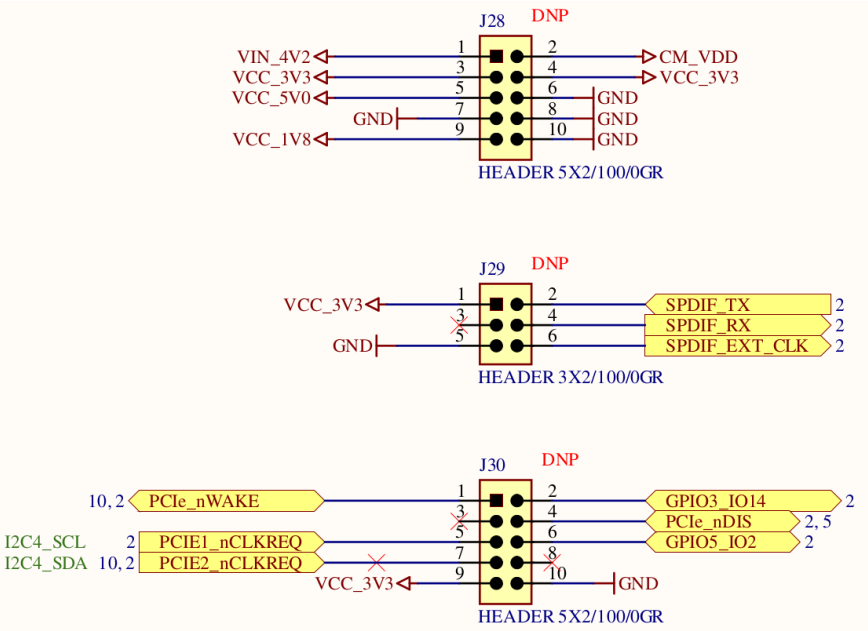


Figure 4.13: GPIO-2

4.14 RTC Clock

i.MX8M-MB implements a I2C RTC Clock connected to I2C1. An external 3.0V battery is required – J26.

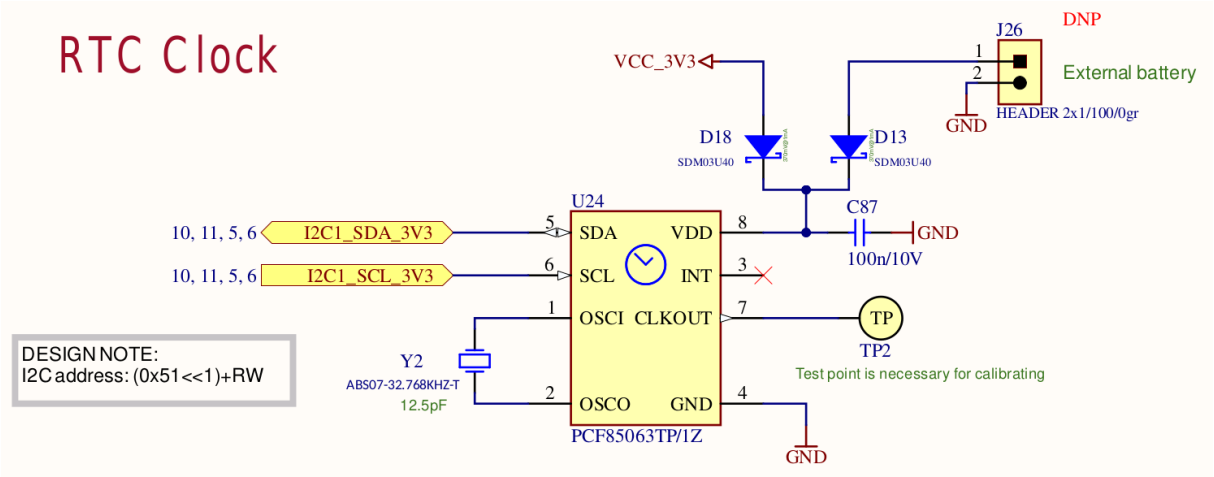


Figure 4.14: RTC Clock

4.15 PCIe M.2

The i.MX8M-MB exposes one M.2 connector - J3. This port can be used for Wi-Fi/Bluetooth cards or some 3G/4G cards.

4.16 JTAG

The i.MX8M-MB exposes the CPU JTAG signals through a standard 1.27mm 10 pin connector – J5.

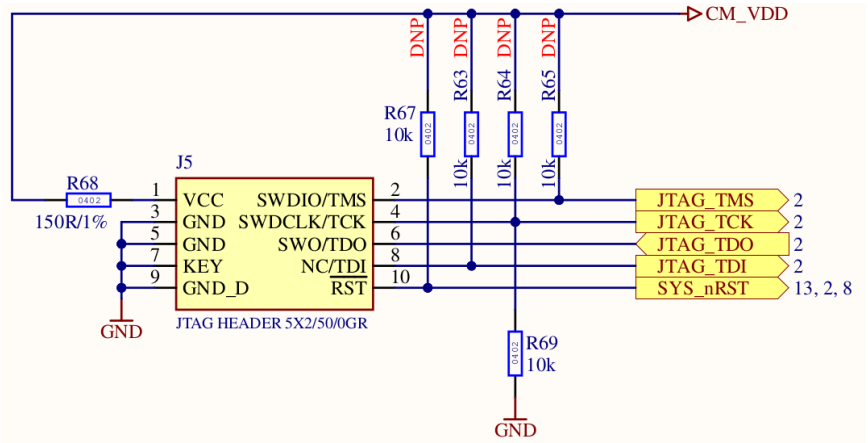


Figure 4.15: JTAG

5. Warranty Terms

Ronetix guarantees hardware products against defects in workmanship and material for a period of one (1) year from the date of shipment. Your sole remedy and Ronetix's sole liability shall be for Ronetix, at its sole discretion, to either repair or replace the defective hardware product at no charge or to refund the purchase price. Shipment costs in both directions are the responsibility of the customer. This warranty is void if the hardware product has been altered or damaged by accident, misuse or abuse.

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